

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				ATTY. DOCKET NO. 1875.2100000		APPLICATION NO. (to be assigned)	
				APPLICANT Keshab K. Parhi		PTO 01/15/01 10/15/01 10/15/01	
				FILING DATE (herewith)			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1						
	AB1						
	AC1						
	AD1						
	AE1						
	AF1						
	AG1						
	AH1						
	AI1						
	AJ1						
	AK1						
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1						Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
LY	AR	1	Parhi, Keshab K. <i>et al.</i> , "Pipeline Interleaving and Parallelism in Recursive Digital Filter - Part I: Pipelining Using Scattered Look-Ahead and Decomposition," <i>IEEE Transaction on Acoustics, Speech, and Signal Processing</i> , Vol. 37, No. 7, July 1989, pp. 1099-1117.				
LY	AS	1	Parhi, Keshab K. <i>et al.</i> , "Static Rate-Optimal Scheduling of Iterative Data-Flow Programs via Optimum Unfolding," <i>IEEE Transactions on Computers</i> , Vol. 40, No. 2, February 1991, pp. 178-195.				
LY	AT	1	Keshab K. Parhi, "Pipelining in Algorithms with Quantizer Loops," <i>IEEE Transactions on Circuits and Systems</i> , Vol. 38, No. 7, July 1991, pp. 745-754.				
EXAMINER					DATE CONSIDERED		
					1/10/2006		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA2						
	AB2						
	AC2						
	AD2						
	AE2						
	AF2						
	AG2						
	AH2						
	AI2						
	AJ2						
	AK2						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

Ly	AR	2	Keshab K. Parhi, "Low-Energy CSMT Carry Generators and Binary Adders," <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , Vol. 7, No. 4, December 1999, pp.450-462.
Ly	AS	2	Kasturia, Sanjay <i>et al.</i> , "Techniques for High-Speed Implementation of Nonlinear Cancellation," <i>IEEE Journal on Selected Areas in Communications</i> , Vol. 9, No. 5, June 1991, pp. 711-717
	AT	2	

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